

2MBI1400VXB-120E-50

IGBT Modules

IGBT MODULE (V series) 1200V / 1400A / 2 in one package

Features

High speed switching Voltage drive Low Inductance module structure

Applications

Inverter for Motor Drive AC and DC Servo Drive Amplifier Uninterruptible Power Supply Industrial machines, such as Welding machines



■ Maximum Ratings and Characteristics

Absolute Maximum Ratings (at Tc=25°C unless otherwise specified)

Items	Symbols	Conditions		Maximum ratings	Units	
Collector-Emitter voltage	Vces				V	
Gate-Emitter voltage	V _{GES}			±20	V	
	Ic	Continuous	Tc=25°C	1800		
interpretation of the second o		Continuous	Tc=100°C	1400		
Collector current	I _{c pulse}	1ms		2800	Α	
	-l _c			1400		
	-I _{c pulse}	1ms		2800		
Collector power dissipation	Pc	1 device		7650	W	
Junction temperature	T _j			175		
Operating junction temperature (under switching conditions)	T _{jop}			150	°C	
Case temperature	Tc			150		
Storage temperature	T _{stg}					
Isolation voltage between terminal and copper base (*1) between thermistor and others (*2)	V _{iso}	AC : 1min	AC : 1min.		VAC	
		AC . IIIIII.		4000	VAC	
Mounting		M5	M5		N m	
Screw torque (*3) Main Terminals	<u> </u> -	M8	M8			
Sense Terminals		M4		2.1		

Note *1: All terminals should be connected together during the test.

Note *2: Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.

Note *3: Recommendable Value: Mounting 3.0 ~ 6.0 Nm (M5) Recommendable Value: Main Terminals 8.0 ~ 10.0 Nm (M8)

Recommendable Value: Sense Terminals 1.8 ~ 2.1 Nm (M4)

● Electrical characteristics (at Tj= 25°C unless otherwise specified)

Items		Cympholo	Symbols Conditions		Characteristics			Halta
		Symbols			min.	typ.	max.	Units
Zero gat	te voltage collector current	Ices	V _{GE} = 0V, V _{CE} = 1200V		-	-	12.0	mA
Gate-En	nitter leakage current	Iges	$V_{CE} = 0V, V_{GE} = \pm 20V$		-	-	2400	nA
Gate-En	nitter threshold voltage	V _{GE (th)}	V _{CE} = 20V, I _C = 1400mA		6.0	6.5	7.0	V
Collector-Emitter saturation voltage	V _{CE} (sat)	V _{GE} = 15V I _C = 1400A	T _j =25°C	-	1.85	2.30		
	(terminal)		T _j =125°C	-	2.15	-	V	
	(*4)		T _j =150°C	-	2.20	-		
	V		T _j =25°C	-	1.75	2.20		
	V _{CE (sat)}		T _j =125°C	-	2.05	-		
	(chip)		T _j =150°C	-	2.10	-		
Internal	gate resistance	Rg _(int)	-		-	0.79	-	Ω
Input ca	pacitance	Cies	V _{CE} = 10V, V _{GE} = 0V, f = 1	-	128	-	nF	
Input capacitance Turn-on time	ton			-	1000	-	nsec	
	tr			-	400	-		
	tr (i)	Vcc=600V, Ic=1400A, VGE=	-	150	-			
Turn-off time	toff	R _G =1Ω, Ls=60nH		-	1200	-		
	tf			-	150	-		
	V _F		T _i =25°C	-	1.90	2.35		
		(terminal)		T _i =125°C	-	2.05	-	1
Forward on voltage	(*4)	V _{GE} = 0V I _F = 1400A	T _i =150°C	-	2.00	-	V	
			T _i =25°C	-	1.80	2.25		
	V _F		T _i =125°C	-	1.95	-		
	(chip)		T _i =150°C	-	1.90	-		
Reverse	recovery time	trr	I _F = 1400A		-	200	-	nsec
D	Resistance B value	Б	T=25°C		-	5000	-	
Kesista		R	T=100°C		465	495	520	Ω
B value		В	T=25/50°C		3305	3375	3450	K

Note *4: Please refer to page 6, there is definition of on-state voltage at terminal.

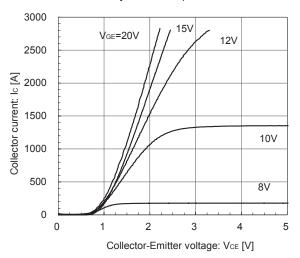
Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
			min.	typ.	max.	Units
Thermal resistance (1device)	Rth(j-c)	Inverter IGBT	-	-	0.0195	°C/W
		Inverter FWD	-	-	0.0360	
Contact thermal resistance (1device) (*5)	Rth(c-f)	with Thermal Compound	-	0.00420	-	

■ Characteristics (Representative)

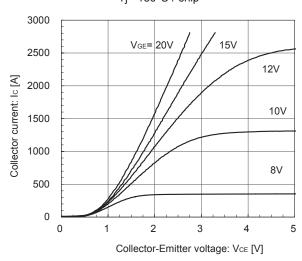
[INVERTER]

Collector current vs. Collector-Emitter voltage (typ.) Tj= 25° C / chip



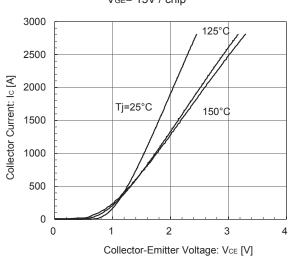
[INVERTER]

Collector current vs. Collector-Emitter voltage (typ.) Tj= 150°C / chip



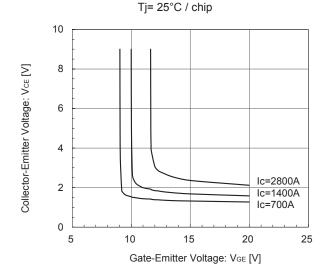
[INVERTER]

Collector current vs. Collector-Emitter voltage (typ.) V_{GE}= 15V / chip



[INVERTER]

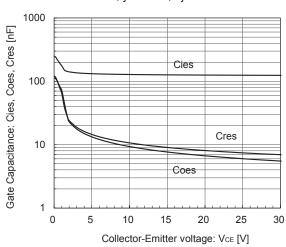
Collector-Emitter voltage vs. Gate-Emitter voltage (typ.)



[INVERTER]

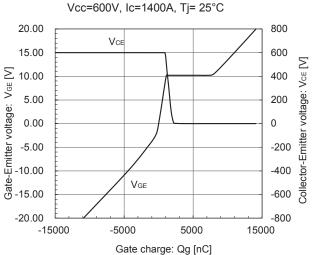
Gate Capacitance vs. Collector-Emitter Voltage (typ.)

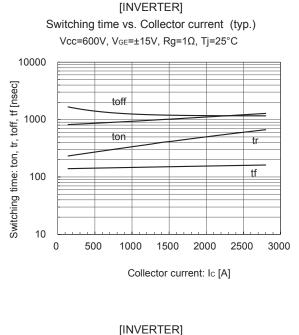
VGE= 0V, f= 1MHz, Tj= 25°C



[INVERTER]

Dynamic Gate Charge (typ.)





Switching time vs. Collector current (typ.) Vcc=600V, V_{GE}=±15V, Rg=1Ω, Tj=125°C, 150°C 10000 Switching time: ton, tr, toff, tf [nsec] toff 1000 tr tf 100 Tj=125°C Tj=150°C 10 0 500 1000 1500 2000 2500 3000 Collector current: Ic [A]

[INVERTER]

Vcc=600V, Ic=1400A, V_{GE}=±15V, Tj=125°C, 150°C

10000

1000

1000

1000

1000

1000

1000

1000

1000

1000

1000

1000

1000

1000

1000

1000

1000

1000

1000

1000

1000

1000

1000

1000

1000

Gate resistance: Rg [Ω]

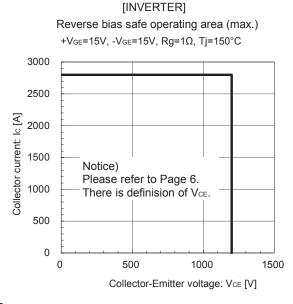
Switching time vs. Gate resistance (typ.)

Switching loss vs. Collector current (typ.) Vcc=600V, VgE= \pm 15V, Rg= 1Ω , Tj=125°C, 150°C 800 Tj=125°C Eoff, Err [mJ/pulse] 700 Tj=150°C 600 500 400 Eoff Eon, 300 Eon Switching loss: 200 100 Err 0 500 1000 1500 2000 2500 3000 Collector current: Ic [A]

[INVERTER]

Switching loss vs. Gate resistance (typ.) Vcc=600V, Ic=1400A, VGE=±15V, Tj=125°C, 150°C 700 Switching loss: Eon, Eoff, Err [mJ/pulse] Tj=125°C 600 Tj=150°C 500 400 **Eoff** 300 Eon 200 Err 100 0 0 10 Gate resistance: Rg $[\Omega]$

[INVERTER]



[INVERTER] Forward Current vs. Forward Voltage (typ.) chip 3000 2500 Tj=25° 150° 125° C 500 0 2 3 0 1 Forward on voltage: V_F [V]

[INVERTER]

Vcc=600V, VGE=±15V, Rg=1Ω, Tj=125°C, 150°C

10000

Tj=125°C

Tj=125°C

Irr

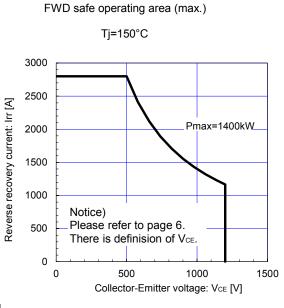
Lim

O 500 1000 1500 2000 2500 3000

Forward current: IF [A]

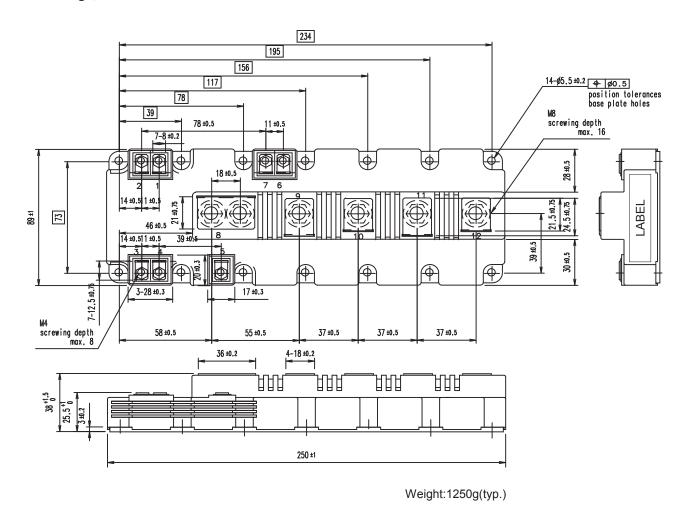
[THERMISTOR]

[INVERTER]
Reverse Recovery Characteristics (typ.)

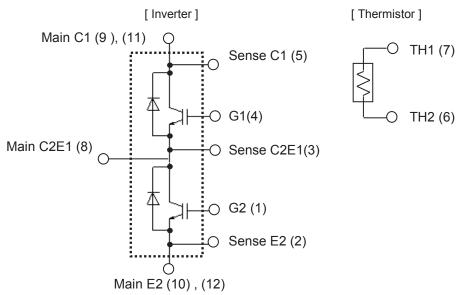


http://www.fujielectric.com/products/semiconductor/

■ Outline Drawings, mm

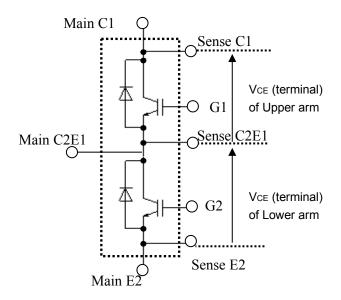


■ Equivalent Circuit Schematic



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■ Definition of on-state voltage at terminal and switching characteristics



Fuji defined V_{CE} value of terminal by using Sense C1 and Sense C2E1 for Upper arm and Sense C2E1 and Sense E2 for Lower arm .

Switching characteristics of V_{CE} also is defined between Sense C1 and Sense C2E1 for Upper arm and Sense C2E1 and Sense E2 for Lower arm .

Please use these terminals whenever measure spike voltage and on-state voltage .

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