Monolithic Amplifier

AVA-24+

5 to 20 GHz 50Ω



CASE STYLE: DQ849

The Big Deal

- Surface Mount Amplifier up to 20 GHz
- •Integrated matching, DC Blocks and bias circuits
- High Reverse Isolation

Product Overview

The Mini-Circuits AVA-24+ is a surface mount, microwave amplifier fabricated using InGaAs PHEMT technology and fully integrated gain block up to 20 GHz. It is packaged in Mini-Circuits industry standard 3x3 mm MCLP (QFN) package, which provides excellent RF and thermal performance. The AVA-24+ integrates the entire matching network with the majority of the bias circuit inside the package, reducing the need for complicated external circuits. This approach makes the AVA-24+ extremely flexible and enables simple, straightforward use.

Key Features

| Feature | Advantages |
|-------------------------|--|
| Wideband, 5 to 20 GHz | Broad frequency range supports a wide array of applications from microwave radio and radar , to military communications and countermeasures. |
| Excellent Gain Flatness | Typical ±0.8 dB gain flatness across the entire frequency range minimizes the need for external equalizer networks making it a great fit for instrumentation and EW applications. |
| High Isolation | With reverse isolation of 37 dB (25 dB directivity), the AVA-24+ is an excellent choice for buffering broadband circuits. It is an ideal LO driver amplifier and provides designers system flexibility and margin when integrating cascaded RF components. |
| Manufacturability | MSL1 and ESD Class1A (HBM) ratings minimize special handling on production lines. |

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Monolithic Amplifier

5-20 GHz

Product Features

- Gain, 12.3 dB typ. & Flatness, ±0.8 dB
- Output Power, up to +18.3 dBm typ.
- Excellent isolation, 37 dB typ.
- Single Positive Supply Voltage, 5V
- Integrated DC blocks, Bias-Tee & Microwave bypass capacitor
- Unconditionally Stable
- Agueous washable: 3mm x 3mm SMT package



+RoHS Compliant

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

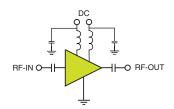
Typical Applications

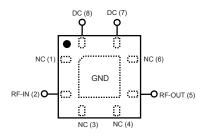
- Military EW and Radar
- DBS
- Wideband Isolation amplifier
- Microwave point-to-point radios
- Satellite systems

General Description

AVA-24+ is a wideband medium power monolithic amplifier fabricated using InGaAs PHEMT technology with outstanding gain flatness up to 20 GHz. It is manufactured using PHEMT* technology and is unconditionally stable. Its outstanding isolation enables it to be used as a wideband isolation amplifier or buffer amplifier in a variety of microwave systems.

simplified schematic and pad description





| Function | Pad Number | Description (See Application Circuit, Fig. 2) |
|----------|---|--|
| RF-IN | 2 | RF input pad |
| RF-OUT | 5 | RF output pad |
| DC | 8(V _{D1}), 7 (V _{D2}) | DC power supply |
| GND | paddle in center of bottom | Connected to ground |
| NOT USED | 1,3,4,6 | No internal connection; recommended use: per PCB Layout PL-328 |

^{*}Pseudomorphic High Electron Mobility Transistor.

USE

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Electrical Specifications⁽¹⁾ at 25°C, Zo=50 Ω , (refer to characterization circuit, Fig. 1)

| Parameter | Condition (GHz) | Min. | Тур. | Max. | Units | | |
|---|-----------------|----------------|----------------|------|-------------|--|--|
| Frequency Range | | 5.0 | | 20.0 | GHz | | |
| DC Voltage (V _{D1} , V _{D2}) | | | 5.0 | | V | | |
| DC Current (I _{D1} +I _{D2}) | | | 134 | 145 | mA | | |
| | 5.0 | | 12.4 | | | | |
| | 8.0 | 10.0 | 12.7 | | | | |
| | 10.0 | 10.0 | 12.4 | | | | |
| Gain | 12.0 | | 12.0 | | dB | | |
| Salli | 14.0 | | 11.7 | | u D | | |
| | 16.0 | | 12.6 | | | | |
| | 18.0 | 10.0 | 12.6 | | | | |
| | 20.0 | 9.0 | 11.5 | | | | |
| | 5.0 | 10.0 | 12.7 | | | | |
| | 8.0 10.0 | 10.0 10.0 | 25.7 17.9 | | | | |
| | 12.0 | 10.0 | 17.9 | | | | |
| nput Return Loss | 14.0 | | 11.3 | | dB | | |
| | 16.0 | 10.0 | 15.8 | | | | |
| | 18.0 | | 11.7 | | | | |
| | 20.0 | | 16.9 | | | | |
| | 5.0 | | 30.0 | | | | |
| | 8.0 10.0 25.0 | | | | | | |
| | 10.0 | 10.0 | 18.6 | | | | |
| Output Beturn Lees | 12.0 | | 14.3 | | -15 | | |
| Output Return Loss | 14.0 | | 13.6 | | dB | | |
| | 16.0 | 10.0 | 20.3 | | | | |
| | 18.0 | | 19.0 | | | | |
| | 20.0 | | 11.5 | | | | |
| | 5.0 | | 26.2 | | | | |
| | 8.0 | | 26.3 | | | | |
| | 10.0 | | 26.5 | | | | |
| Output IP3 | 12.0 14.0 | | 26.3 26.0 | | dBm | | |
| NOI | 16.0 | $-\Lambda$ $-$ | 25.4 | | | | |
| | 18.0 | AL | 24.5 | | | | |
| | 20.0 | | 23.8 | | | | |
| | 5.0 | | 18.8 | | | | |
| | 8.0 | | 18.7 | | | | |
| | 10.0 | 16.0 | 18.6 | | | | |
| Output Power @ 1 dB compression | 12.0 | | 18.7 | | dBm | | |
| Julput Fower @ 1 db compression | 14.0 | | 18.5 | | ubiii | | |
| | 16.0 | | 18.0 | | | | |
| | 18.0 | | 17.4 | | | | |
| | 20.0 | | 18.6 | | | | |
| | 5.0 | | 7.0 | | | | |
| | 8.0 | | 6.3 | | | | |
| Noise Figure | 10.0 12.0 | | 5.6 6.5 | | dB | | |
| Noise Figure | 14.0 | | 6.5 | | ub | | |
| | 16.0 | | 6.1 | | | | |
| | 18.0 | | 6.1 | | | | |
| | 20.0 | | 6.7 | | | | |
| | | 1 | | | | | |
| Directivity (Isolation-Gain) | | | 25.0 | | dB | | |
| Directivity (Isolation-Gain) DC Current Variation vs. Temperature (2) | | | 25.0 -0.087 | | dB mA/°C | | |

Absolute Maximum Ratings(3)

| ADSOIDLE MAXIIIIUIII HALIIIYS | | | | |
|-------------------------------|----------------|--|--|--|
| Parameter | Ratings | | | |
| Operating Temperature (4) | -40°C to 85°C | | | |
| Storage Temperature | -55°C to 100°C | | | |
| Channel Temperature | 160°C | | | |
| DC Voltage (Pad 7,8) | 5.5V | | | |
| Voltage (Pads 2, 5) | 10V | | | |
| Power Dissipation | 860 mW | | | |
| DC Current (Pad 7+8) | 160mA | | | |
| Input Power | 20 dBm | | | |

⁽¹⁾ Measured on Mini-Circuits Characterization test fixture TB-547-1+

See Characterization Test Circuit (Fig. 1)

(2) (Current at 85°C - Current at -45°C)/130

(3) Permanent damage may occur if any of these limits are exceeded. These maximum ratings are not intended for continuous normal operation.

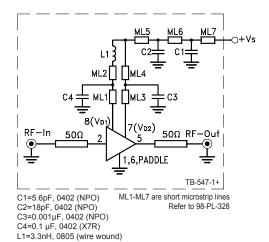
⁽⁴⁾ Defined with reference to ground pad temperature.

Notes
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Characterization Test Circuit

Recommended Application Circuit

(refer to evaluation board for PCB Layout and component values)



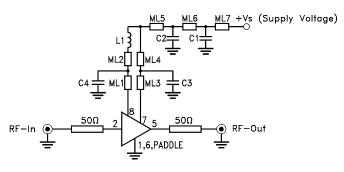


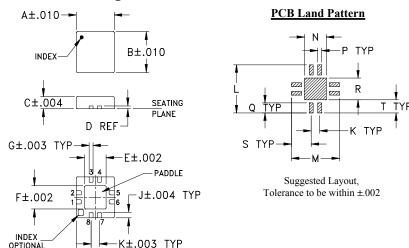
Fig 2. Recommended Application Circuit

Fig 1. Block Diagram of Test Circuit used for characterization. (DUT soldered on Mini-Circuits Characterization Test Board TB-547-1+) Gain, Output power at 1dB compression (P1dB), Noise Figure, Output IP3 (OIP3) are measured using Agilent's N5242A PNA-X microwave network analyzer.

Conditions:

- 1. Gain: Pin=-25 dBm
- 2. Output IP3 (OIP3): Two tones, spaced 1 MHz apart, -10 dBm/tone at output.
- 3. Vs adjusted for 5V at device (V_{D1} and V_{D2}), compensating loss of bias lines.

Outline Drawing



Outline Dimensions (inch mm)

| | J | Н | G | F | E | D | С | В | Α |
|-------|-------|------|------|------|------|------|------|------|------|
| | .016 | .046 | .012 | .067 | .067 | .008 | .035 | .118 | .118 |
| | 0.41 | 1.17 | 0.30 | 1.70 | 1.70 | 0.20 | 0.89 | 3.00 | 3.00 |
| wt | Т | S | R | Q | Р | Ν | M | L | K |
| grams | .041 | .061 | .067 | .031 | .012 | .067 | .148 | .148 | .026 |
| 0.02 | 4 0 4 | 4 55 | 4 70 | 0.70 | 0.00 | 4 70 | 0.70 | 0.70 | 0.00 |

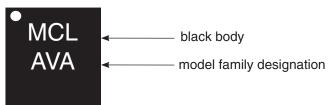
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Product Marking



Additional Detailed Technical Information

Additional information is available on our web site www.minicircuits.com. To access this information enter the model number on our web site home page.

Performance data, graphs, s-parameter data set (.zip file)

Case Style: DQ849

Plastic package, exposed paddle, lead finish: tin silver nickel

Tape & Reel: F104

Standard quantities available on reel: 7" reels with 10, 20, 50, 100, 200, 500,1K, 2K

Suggested Layout for PCB Design: PL-328

Evaluation Board: TB-547-1+

Environmental Ratings: ENV08T1

ESD Rating

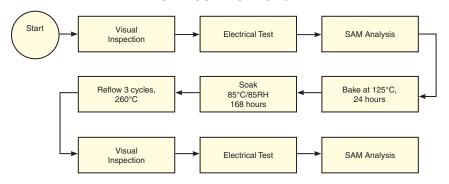
Human Body Model (HBM): 1A (250 to <500V) in accordance with ANSI/ESD STM 5.1 - 2001

Machine Model (MM): M1 (<100V) in accordance with ANSI/ESD STM5.2-1999; passes 25V

MSL Rating

Moisture Sensitivity: MSL1 in accordance with IPC/JEDEC J-STD-020D

MSL Test Flow Chart



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